

Report 11297  
26 October 1998

**GENCORP**  
**AEROJET**

**Integrated Advanced Microwave Sounding Unit-A  
(AMSU-A)**

**Engineering Test Report**

**METSAT A2 Signal Processor (P/N 1331120-2, S/N F03)**

**S/N 107**

**Contract No. NAS 5-32314  
CDRL 207**

**Submitted to:**

**National Aeronautics and Space Administration  
Goddard Space Flight Center  
Greenbelt, Maryland 20771**

**Submitted by:**

**Aerojet  
1100 West Hollyvale Street  
Azusa, California 91702**

**Aerojet**

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**Integrated**  
**Advanced Microwave Sounding Unit-A (AMSU-A)**  
**METSAT A2 Signal Processor Engineering Test Report**  
**(P/N: 1331120-2, S/N: F03) ( Instr. S/N 107 )**

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## TABLE OF CONTENTS

|     |                     |   |
|-----|---------------------|---|
| 1.0 | INTRODUCTION.....   | 1 |
| 2.0 | OBJECTIVE .....     | 1 |
| 3.0 | TEST DATA .....     | 1 |
| 4.0 | TEST .....          | 1 |
| 5.0 | TEST ANOMALIES..... | 5 |
| 6.0 | TEST RESULTS.....   | 5 |

## **1.0 Introduction**

This report presents a description of the tests performed, and the test data, for the A2 METSAT Signal Processor Assembly PN: 1331120-2, S/N F03. The assembly was tested in accordance with AE-26754, "METSAT Signal Processor Scan Drive Test and Integration Procedure".

The tests were conducted at room temperature in the AMSU-A test area of building 57. The tests fall into six categories: 1) Continuity, 2) Power Distribution, 3) Digital Processor, 4) Analog Processor, 5) Scan Drive, and 6) Supply Current.

## **2.0 Objective**

The objective is to demonstrate functionality of the signal processor prior to instrument integration.

## **3.0 Test Data**

All test data is presented on the enclosed copies of the test data sheets (TDSs) numbered TDS 11 through TDS 20 ( Pages A-15 through A-25 ). TDS 11 ( Pg. A15 ) was redlined to incorporate a design change defined in ECN CAMSU-1930. The redline was accomplished in accordance with program directive No. 91 and approved by Quality and the test engineer.

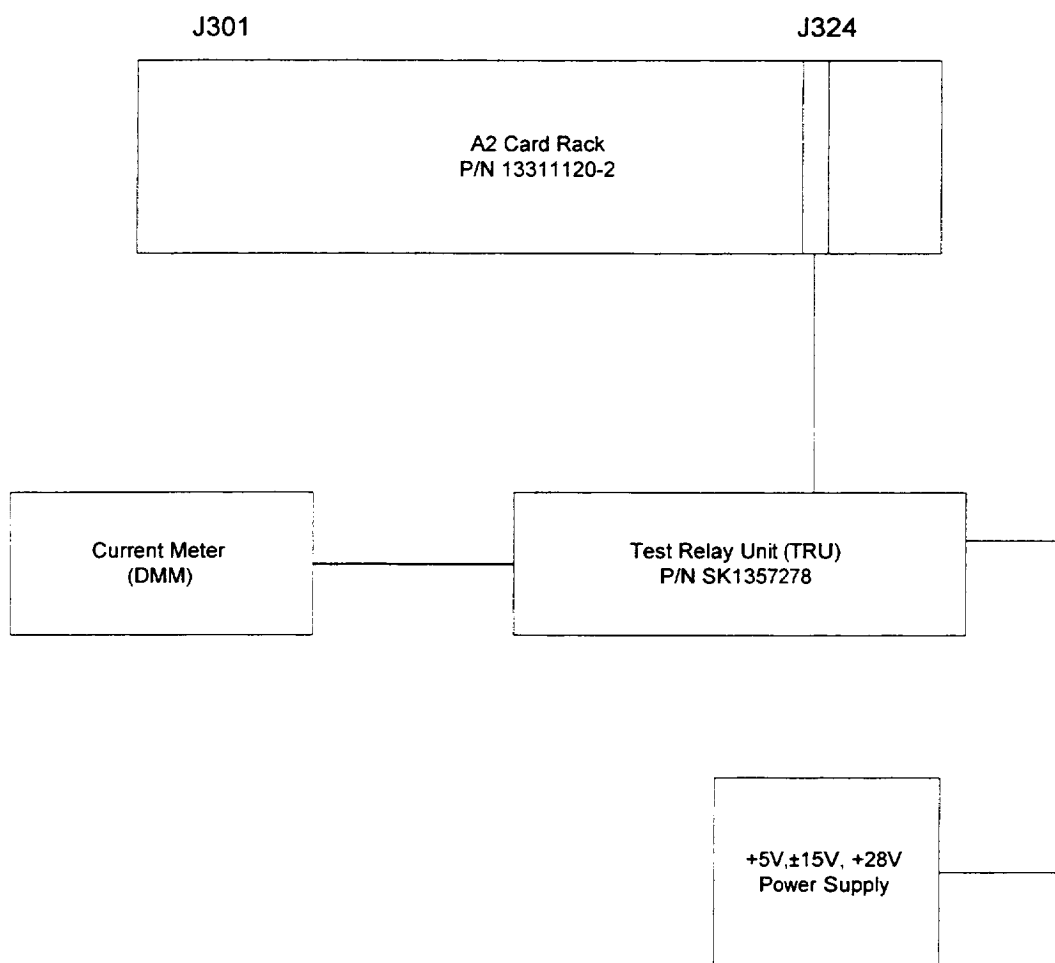
## **4.0 TESTS**

### **4.1 Continuity**

A complete continuity test of the backplane wiring is performed at the facility where the wirewrapping of the backplane is done. The continuity tests performed here involve 1) the I/O interface card slots, J301 and J324, and 2) chassis return connections. The tests are manual resistance measurement tests. Test data is presented on TDS 11.

### **4.2 Power Distribution**

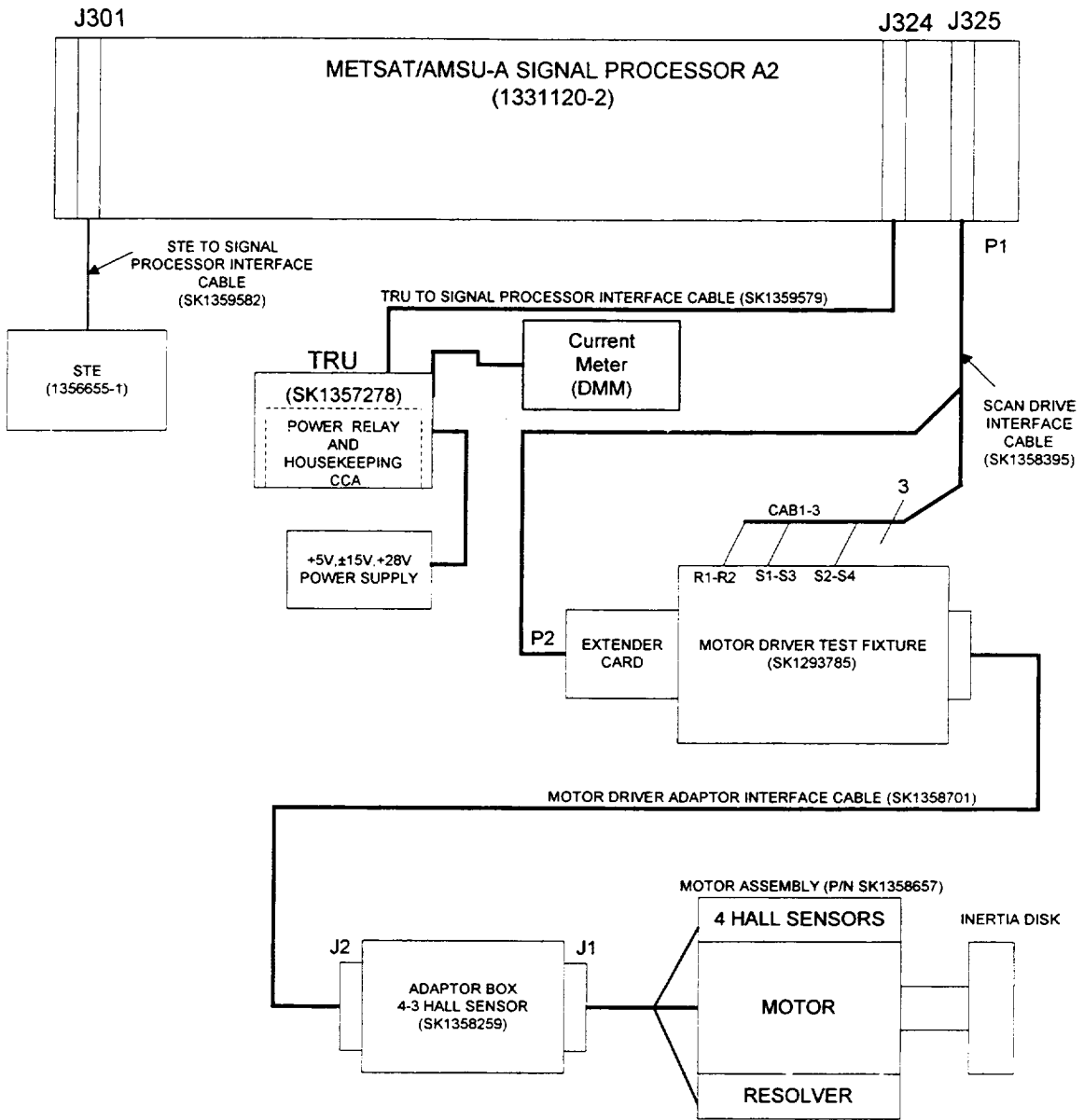
In these tests supply voltages are input to the signal processor from the Test Relay Unit (TRU) as in normal testing. No CCAs are installed in the signal processor for the tests. The test verifies that the four supply voltages are present on the proper pins of all backplane connectors. The test setup block diagram is shown in Figure 1, and test data is presented on TDS 12.



**Figure 1. A2 Signal Processor Test Setup**

#### **4.3 Digital Processor**

Beginning with this test, CCAs are installed into the card cage as required to perform the test, and then remain installed. At the conclusion of all tests, a complete set of CCAs has been installed. The complete test setup block diagram which is required for performing any of the tests is shown in Figure 2.



**Figure 2. A2 Scan Drive Test Setup**

#### **4.3.1 Memory**

In this test, the digital test set is used in place of the CPU CCA to read and verify data of the test PROMs on the "GOLD" Memory CCA. Test data is presented on TDS 13.

#### **4.3.2 CPU**

The CPU test requires that the CPU Auxiliary test CCA be installed in place of the Memory CCA. In this test, the RAM and various instructions performed by the CPU are tested. In addition, the waveform of the clock signal to the DC-DC converter is measured at the CLOCK jack on the TRU. Test data is presented on TDS 13.

#### **4.3.3 Scan Control Interface**

In this test, input and output ports 0 through 3 are tested. In addition, the disable feature of the input ports is checked out. Test data is presented on TDS 13.

#### **4.3.4 Timing and Control**

In this test, the proper time intervals of I/H, DUMP, INTCMPL, TSCMPL, STOP, and ANTENNA STROBE are verified. In addition to the above tests, the test set also checks the input ports 16 and 17, output port #13 (4 MSBs), output port 14, input port #15 (DAC BSY signal), and output port #13 (4 LSBs). Test data is presented on TDS 13.

#### **4.3.5 Spacecraft Interface**

In this test, the STE is turned on and initialized. The STE is tested with a series of self-tests to verify the readiness of the STE to test flight hardware. After successfully passing the self-tests, the STE is used to simulate the spacecraft command signals and retrieve limited test data for the remaining signal processor tests. STE test data is presented on TDS 14.

#### **4.3.6 Relay Control**

This test verifies the operation of the module power command and the survival heater command. The presence of the +10 volt Interface power is verified. The Scanner and Compensator relay drive and position indicators are also verified. Test data is presented on TDS 14.

### **4.4 Analog Processor**

#### **4.4.1 Independence of Measurements**

This test is performed using the Analog CCA Test Fixture, the Integrate and Dump Filter and the Analog Mux and A/D Converter CCAs. The test gives a measurement of the sample-to-sample crosstalk within a channel, which is dependent on the completeness of the dump of the integration capacitor. Test data is presented on TDS 15.

#### **4.4.2 Integrate/dump filter, radiometric data multiplexing, and digitization tests**

In this test, a 2 volt dc signal is input to each integrate and dump filter, and the channel output code from the A/D converter is measured. The integrator output waveform is also displayed on an oscilloscope for verification of timing. Test data is presented on TDS 16.

#### **4.4.3 Temperature monitoring circuits**

In this test a resistor of value approximating the room temperature of the PRTs is connected at the input of each PRT readout circuit, and the output code from the A/D converter is measured. The reference voltage used in the PRT readout circuits is also measured.. Test data is presented on TDS 17.

#### **4.4.4 Analog telemetry**

In this test each of the analog telemetry signals is measured at the ANALOG HSKP jack on the TRU. Test data is presented on TDS 18.

#### **4.5 Scan Drive**

This test includes all CCAs involved in the scan drive function. The circuitry is programmed to provide one complete revolution of the drive motor as it steps through each of the thirty scene positions and the two calibration positions. The circuitry is programmed to park at the Warm Cal, Cold Cal, and the Nadir positions during the test sequence. The GSE test modes are also verified. To verify proper performance, the inertia disk on the motor shaft is visually observed through the one revolution and the various calibration positions. Test data is presented on TDS 19.

#### **4.6 Supply Current**

In this test, the total current drawn by the signal processor from each of the four supply voltages is measured with the signal processor fully populated with CCA's. Test data is presented on TDS 20.

#### **5.0 TEST ANOMALIES**

No test anomalies occurred during the Signal Processor engineering tests.

#### **6.0 TEST RESULTS**

The METSAT/AMSU A2 SIGNAL PROCESSOR TEST was successfully completed and all test data is within specified limits.

11 June 98

**TEST DATA SHEET 11**  
A2 Continuity Tests (Paragraph 5.2.1)

Enter a Pass or Fail to indicate the result of the tests:

| From               | To                 | Signal Name                          | Pass/Fail    |
|--------------------|--------------------|--------------------------------------|--------------|
| E1                 | J301-60            | CHASSIS GND                          | P            |
| E2                 | J301-90            | CHASSIS GND                          | P            |
| E4                 | J302-46            | CHASSIS GND                          | P            |
| E3                 | J324-76            | CHASSIS GND                          | P            |
| J324-73            | J312-70            | 1.248 MHZ PS CLK                     | P            |
| J324-74            | J312-89            | 5V RTN(1) (1.248 MHZ PS CLK RTN)     | P            |
| <del>J324-75</del> | <del>J312-91</del> | <del>5V RTN(1) (PS CLK SHIELD)</del> | <del>P</del> |

*Deleted per ECN CAMSU-1930*

*Done*

*9/17/98*

QC  
227

Assembly No. 1331120-2Shop Order No. 496332Serial No. F03Pass ☒ Fail ☐Test Engineer *Done* 9/23/98  
(Signature) (Date)Quality Control *[Signature]* SEP 23 98  
(Signature) (Date)Customer Representative (Flight hardware only) *[Signature]*  
(Signature)SEP 24 98  
(Date)

**TEST DATA SHEET 12**  
**A2 Power Distribution (Paragraphs 5.2.2 & 5.2.3)**

Power Supply Voltages:

+5.7 ± 0.1V: +5.692  
+15.7 ± 0.1V: +15.694  
-15.7 ± 0.1V: -15.681  
+28.7 ± 0.1V: +28.74

Test Set-up Verified: YES ☒ NO ☐

| Para.<br>5.2.3<br>Step No. | Connector<br>No. | +5<br>±0.5V | P/F | +15<br>±0.3V | P/F | -15<br>±0.3V | P/F | +28<br>±0.56V | P/F | +9<br>±1V* | P/F |
|----------------------------|------------------|-------------|-----|--------------|-----|--------------|-----|---------------|-----|------------|-----|
| 7*                         | J301             |             |     |              |     |              |     |               |     | 9.436      | P   |
| 2                          | J302             |             |     | 14.986       | P   | -14.99       | P   |               |     |            |     |
| 3                          | J303             |             |     | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 4                          | J304             |             |     | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 5                          | J305             |             |     | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 6                          | J306             | 4.87        | P   | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 6                          | J307             |             |     | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 6                          | J308             | 4.87        | P   |              |     |              |     |               |     | 9.436      | P   |
| 6                          | J309             | 4.90        | P   |              |     |              |     |               |     | 9.436      | P   |
| 6                          | J310             | 4.90        | P   |              |     |              |     |               |     |            |     |
| 6                          | J311             | 4.90        | P   |              |     |              |     |               |     |            |     |
| 6                          | J312             | 4.90        | P   |              |     |              |     |               |     |            |     |
| 6                          | J313             | 4.90        | P   |              |     |              |     |               |     |            |     |
| 6                          | J315             | 4.90        | P   |              |     |              |     |               |     |            |     |
| 6                          | J317             | 4.90        | P   | 14.98        | P   | -14.99       | P   | 27.98         | P   |            |     |
| 6                          | J318             | 4.90        | P   | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 6                          | J320             | 4.90        | P   |              |     |              |     |               |     |            |     |
| 6                          | J321             | 4.92        | P   | 14.98        | P   | -14.99       | P   |               |     |            |     |
| 6                          | J322             | 4.92        | P   | 14.98        | P   | -14.99       | P   | 27.97         | P   |            |     |
| 6                          | J323             | 4.92        | P   | 14.98        | P   | -14.99       | P   | 27.96         | P   |            |     |
| 7                          | J325             |             |     |              |     |              |     | 27.97         | P   |            |     |

\*measured at paragraph 5.2.5.2. test

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F-03

Pass ☒ Fail ☐

Test Engineer [Signature] 9/23/98  
(Signature) (Date)

Quality Control [Signature] SEP 23 '98  
(Signature) (Date)

Customer Representative (Flight hardware only) [Signature] SEP 24 '98  
(Signature) (Date)

**TEST DATA SHEET 13** (Sheet 1 of 2)  
A2 Digital Processor (Paragraph 5.2.4)

CPU CCA Serial No. (J312) F06  
Scan Control Interface CCA Serial No. (J315) F22  
Timing and Control CCA Serial No. (J311) F08

**5.2.4.1 Memory tests:**

5.2.4.1/10 Circle PASS or FAIL to indicate the result of the tests:

Pass Fail

If "Fail", record the error code and error description.

Error Code: N/A

Error Description: N/A

**5.2.4.2 CPU tests:**

| 5.2.4.2/10 | <u>Measurements</u> | <u>Limits</u> | <u>Pass/Fail</u>                        |
|------------|---------------------|---------------|---|
| Vp-p       | <u>3.65 Vpp</u>     | 3.30 - 4.94 V | <u>P</u>                                |
| T          | <u>802.5 ns</u>     | 761 - 841 ns  | <u>P</u><br><del>802.5 ns</del> 9/23/98 |

5.2.4.2/19 Circle PASS or FAIL to indicate if LEDs indicate CCA passed or failed:

Pass Fail

**5.2.4.3 Scan Control Interface Tests:**

5.2.4.3/14 The input ports 0 and 1 tests

Pass Fail

5.2.4.3/21 Inhibit input port 0 and 1 tests

Pass Fail

5.2.4.3/29 The input ports 2 and 3 tests

Pass Fail

5.2.4.3/41 The output ports 0 and 1 tests

Pass Fail

If "Fail", record the error code and error description.

Error Code: N/A

Error Description: N/A

TEST DATA SHEET 13 (Sheet 2 of 2)  
A2 Digital Processor (Paragraph 5.2.4)

5.2.4.4 Timing and Control Tests:

|            |  |             |      |
|------------|--|-------------|------|
| 5.2.4.4/13 | The Integrate and Hold pulse and the Dump pulse at the card rack slot J307.    | <u>Pass</u> | Fail |
| 5.2.4.4/25 | The Integrate and Hold pulse and the Dump pulse at the card rack slot J301.    | <u>Pass</u> | Fail |
| 5.2.4.4/35 | The Antenna Strobe pulse test.   | <u>Pass</u> | Fail |
| 5.2.4.4/47 | The test of the interface to the Temp. Sensor Analog Mux card rack slot J303.  | <u>Pass</u> | Fail |
| 5.2.4.4/59 | The test of the interface to the Analog Mux and Converter card rack slot J308. | <u>Pass</u> | Fail |

If "Fail", record error code and error description:

Error Code:

N/A

Error Description:

N/A

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F03

Pass ☒ Fail ☐

Test Engineer D. Lund 9/23/98  
(Signature) (Date)

Quality Control [Signature] SEP 23 '98  
(Signature) (Date)

Customer Representative (Flight hardware only) J. Lund SEP 24 '98  
(Signature) (Date)

**TEST DATA SHEET 14**  
A2 Relay Driver Tests (Paragraph 5.2.5.2)

Spacecraft Interface #2 CCA (J308) Ser. No. F17  
Spacecraft Interface #1 CCA (J309) Ser. No. F17  
Parallel to Serial Converter CCA (J310) Ser. No. F18  
Relay Driver And Current Monitor CCA (J317) Ser. No. F04

Test Set-up Verified: Yes ☒ No ☐ STE Self Test: Pass ☒ Fail ☐

| Step No. | Test Description                  | Pass/Fail |
|----------|-----------------------------------|-----------|
| 24       | Module power connects             | P         |
| 30       | Survival heater power turns on    | P         |
| 31       | Survival heater power turns off   | P         |
| 32       | Module power disconnects          | P         |
| 34       | Scanner 2 power turns on          | P         |
| 35       | Compensator motor power turns on  | P         |
| 36       | Scanner 2 power turns off         | P         |
| 36       | Compensator motor power turns off | P         |
| 37       | Module power disconnect           | P         |

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F03

Pass ☒ Fail ☐

Test Engineer D. Lush 9/23/98  
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Customer Representative (Flight hardware only) [Signature] SEP 24 '98  
(Signature) (Date)

**TEST DATA SHEET 15**  
**A2 Independence Of Measurements (Paragraph 5.2.6.1)**

Integrate and Dump CCA (J307): Serial No. F31

Analog Mux and A/D Converter CCA (J306): Serial No. F11

Test Set-up verified: YES ☒ NO ☐

| <u>Supply (V)</u> | <u>Measured Value (V)</u> | <u>Limits (V)</u> |
|-------------------|---------------------------|-------------------|
| +5                | <u>4.8 V</u>              | +5 ± 0.25         |
| +15               | <u>15.846V</u>            | +15 ± 1.0         |
| -15               | <u>-15.426V</u>           | -15 ± 1.0         |

| Channel No. | Average for SIGNAL switch in Hi position | Average for SIGNAL switch in LO position | Measurement Dependence ≤0.01% | Pass/Fail |
|-------------|--|--|-------------------------------|-----------|
| 0           | 14096                                    | 14094.5                                  | 0.00229                       | P         |
| 1           | 14098.6                                  | 14097                                    | 0.00244                       | P         |
| 2           | 14110.5                                  | 14108.9                                  | 0.00244                       | P         |
| 3           | 14104                                    | 14102.2                                  | 0.00275                       | P         |

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F03

Pass ☒ Fail ☐

Test Engineer D. Lund 9/23/98  
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(Signature) (Date)

**TEST DATA SHEET 16**  
A2 Integrator Signal Multiplexing, And Digitization (Paragraph 5.2.6.2)

Analog Mux and A/D Converter CCA(J306): Ser. No. F11

Integrate and Dump/Filter CCA (J307): Ser. No. F31



| Channel | Data  | Data Limits    | Data Pass/Fail | Integrator Waveform Pass/Fail |
|---------|-------|----------------|----------------|-------------------------------|
| 1       | 27744 | 26125 to 29757 | P              | P                             |
| 2       | 27827 | 26125 to 29757 | P              | P                             |

| Signal Name            | Pass/Fail |
|------------------------|-----------|
| I/H                    | P         |
| Dump                   | P         |
| +5 Vdc GSE Interlock A | P         |
| +5 Vdc GSE Interlock B | P         |

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F03

Pass ☒ Fail ☐

Test Engineer [Signature] 9/23/98  
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Quality Control [Signature] SEP 23 '98  
(Signature) (Date)

Customer Representative (Flight hardware only) [Signature] SEP 24 '98  
(Signature) (Date)

**TEST DATA SHEET 17**  
**A2 Temperature Monitoring Circuits (Paragraph 5.2.6.3)**

Temperature Sensor Analog Mux CCA (J303) Serial No. F20

Temperature Sensor B CCA (J304) Serial No. F31

Temperature Sensor A CCA (J305) Serial No. F07

| Dig. A Temp No. | Description       | Data  | Data Limits    | Pass/Fail |
|-----------------|-------------------|-------|----------------|-----------|
| 1               | Scan Motor        | 31282 | 28259 to 32513 | P         |
| 2               | Feedhorn          | 31189 | 28259 to 32513 | P         |
| 3               | RF MUX            | 31269 | 28259 to 32513 | P         |
| 4               | Mixer IF CH 1     | 31446 | 28259 to 32513 | P         |
| 5               | Mixer IF CH 2     | 31244 | 28259 to 32513 | P         |
| 6               | LO Channel 1      | 31367 | 28259 to 32513 | P         |
| 7               | LO Channel 2      | 31174 | 28259 to 32513 | P         |
| 8               | Comp Motor        | 31317 | 28259 to 32513 | P         |
| 9               | Subreflector      | 31070 | 28259 to 32513 | P         |
| 10              | Dc/Dc Converter   | 31263 | 28259 to 32513 | P         |
| 11              | RF Shelf          | 31514 | 28259 to 32513 | P         |
| 12              | Det/Preamp        | 31245 | 28259 to 32513 | P         |
| 13              | Warm Load Cntr    | 22848 | 20339 to 23401 | P         |
| 14              | Warm Load 1       | 22488 | 20339 to 23401 | P         |
| 15              | Warm Load 2       | 22672 | 20339 to 23401 | P         |
| 16              | Warm Load 3       | 22208 | 20339 to 23401 | P         |
| 17              | Warm Load 4       | 22508 | 20339 to 23401 | P         |
| 18              | Warm Load 5       | 22513 | 20339 to 23401 | P         |
| 19              | Warm Load 6       | 22518 | 20339 to 23401 | P         |
| 20              | Thermal Reference | 25233 | 23340 to 26320 | P         |

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F03

Pass ☒ Fail ☐

Test Engineer [Signature] 9/23/98  
(Signature) (Date)

Quality Control [Signature] SEP 23 '98  
(Signature) (Date)

Customer Representative (Flight hardware only) [Signature] SEP 24 '98  
(Signature) (Date)

**TEST DATA SHEET 18**  
A2 Analog Telemetry (Paragraph 5.2.6.4)

| ANALOG HSKP<br>Switch Position | DVM Reading<br>(V)     | Limits (V)    | Pass/Fail |
|--------------------------------|------------------------|---------------|-----------|
| 1                              | 2.953                  | 2.85 to 3.15  | P         |
| 2                              | 3.463                  | 3.30 to 3.66  | P         |
| 3                              | 2.984                  | 2.87 to 3.17  | P         |
| 4                              | 2.976                  | 2.85 to 3.15  | P         |
| 5                              | 3.456                  | 3.30 to 3.66  | P         |
| 6                              | 2.995                  | 2.87 to 3.17  | P         |
| 10                             | 3.573                  | 3.42 to 3.78  | P         |
| 12                             | <del>3.267</del> 2.967 | 2.84 to 3.14  | P         |
| 13                             | 2.966                  | 2.84 to 3.14  | P         |
| 21                             | 0.0067                 | -0.05 to 0.05 | P         |
| 21                             | 2.958                  | 2.8 to 3.4    | P         |
| 22                             | 0.0077                 | -0.05 to 0.05 | P         |
| 22                             | 2.956                  | 2.8 to 3.4    | P         |

Assembly No. 1331120-2

Shop Order No. 496332

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Pass ☒ Fail ☐

Test Engineer [Signature] 9/23/98  
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**TEST DATA SHEET 19**  
A2 Scan Drive/Compensator Drive/Signal Processor Tests (Paragraph 5.3.1)

**A2 Scan Drive Subsystem CCAs:**

Interface Converter CCA (J318) Ser. No. F31  
Resolver Data Isolator CCA (J320) Ser. No. F32  
R/D Converter/Oscillator CCA (J321) Ser. No. F07  
Motor Drive 3-hall sensor CCA (J322) Ser. No. F04

Test Set-up Verified: Yes ☒ No ☐

| Para./Step No. | Mode                         | Pass/Fail |
|----------------|------------------------------|-----------|
| 5.3.1.2.1/12   | Motor in warm cal position   | P         |
| 5.3.1.2.2/3    | Motor in nadir position.     | P         |
| 5.3.1.2.3/2    | Motor in cold cal position 1 | P         |
| 5.3.1.2.3/3    | Motor in cold cal position 2 | P         |
| 5.3.1.2.3/4    | Motor in cold cal position 3 | P         |
| 5.3.1.2.3/5    | Motor in cold cal position 4 | P         |
| 5.3.1.2.4/5    | Motor in full scan mode      | P         |
| 5.3.1.2.5/9    | GSE mode 2                   | P         |
| 5.3.1.2.6/4    | GSE mode 4                   | P         |
| 5.3.1.2.7/4    | GSE mode 5                   | P         |
| 5.3.1.2.8/4    | GSE mode 1                   | P         |
| 5.3.1.2.9/4    | GSE mode 3                   | P         |
| 5.3.1.2.9/7    | GSE mode 7                   | P         |
| 5.3.1.2.10/2   | Scan power off               | P         |

**A2 Compensator Drive Subsystem CCAs:**

Motor Driver 3-hall Sensor CCA (J323) Ser. No. F05

Test Set-up Verified: Yes ☒ No ☐

| Para./Step No. | Mode                                | Pass/Fail |
|----------------|-------------------------------------|-----------|
| 5.3.2.2/4      | Compensator motor operation         | P         |
| 5.3.2.2/5      | Power-off test of compensator motor | P         |

Assembly No. 1331120-2

Shop Order No. 496332

Serial No. F03

Pass ☒ Fail ☐

Test Engineer D. Lusk 9/23/98  
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(Signature) (Date)

Customer Representative (Flight hardware only) [Signature]  
(Signature)

SEP 24 98  
(Date)

**TEST DATA SHEET 20**  
**A2 Supply Currents (Paragraph 5.4)**

| Voltages | Measured Current | Limits (in mA) | Pass/Fail |
|----------|------------------|----------------|-----------|
| +28.7 V  | 7.398 mA         | 6 to 12        | P         |
| +5.7 V   | 466 mA           | 400 to 700     | P         |
| +15.7 V  | 132 mA           | 100 to 196     | P         |
| -15.7 V  | -155 mA          | -110 to -218   | P         |

Assembly No. 1331120-2

Shop Order No. 496332


Serial No. F03

Pass ☒ Fail ☐

Test Engineer [Signature] 9/23/98  
(Signature) (Date)

Quality Control [Signature] SEP 23 98  
(Signature) (Date)

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| 15. Supplementary Notes<br><br>---  |  |  |  |  |  |                      |  |
| 16. ABSTRACT (Maximum 200 words )<br><br>This is the Engineering Test Report, METSAT A2 Signal Processor (P/N 1331120-2, S/F F03), S/N 107, for the Integrated Advanced Microwave Sounding Unit-A (AMSU-A). |  |  |  |  |  |                      |  |
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| 6. AUTHOR(S)<br>D. Lund   |   |  |   |                                  |
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| TITLE<br><b>Engineering Test Report</b><br><b>METSAT A2 Signal Processor (P/N 1331120-2, S/N F03), S/N 107</b>  |      | DOCUMENT NO.<br><b>Report 11297</b><br><b>26 October 1998</b> |                                       |
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| Systems Engineer (R. Platt) <u><i>R. Platt</i></u>  |      | 8311  | 10/27/98                              |
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| 13. ABSTRACT (Maximum 200 words)<br><br>This report presents a description of the tests performed, and the test data, for the A2 METSAT Signal Processor Assembly PN: 1331120-2, S/N F03. The assembly was tested in accordance with AE-26754, "METSAT Signal Processor Scan Drive Test and Integration Procedure."  |   |  |   |   |
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